FIG. 1 90 85 Tub Drive Controls CPU Memory 75-Cycle Controls Volatile Memory 98-50-53 20

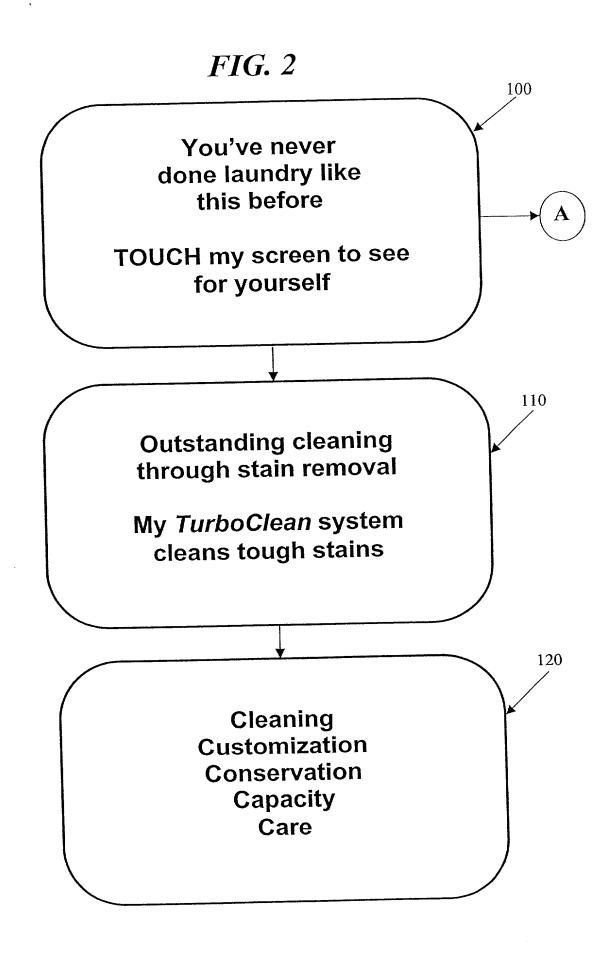


FIG. 3A

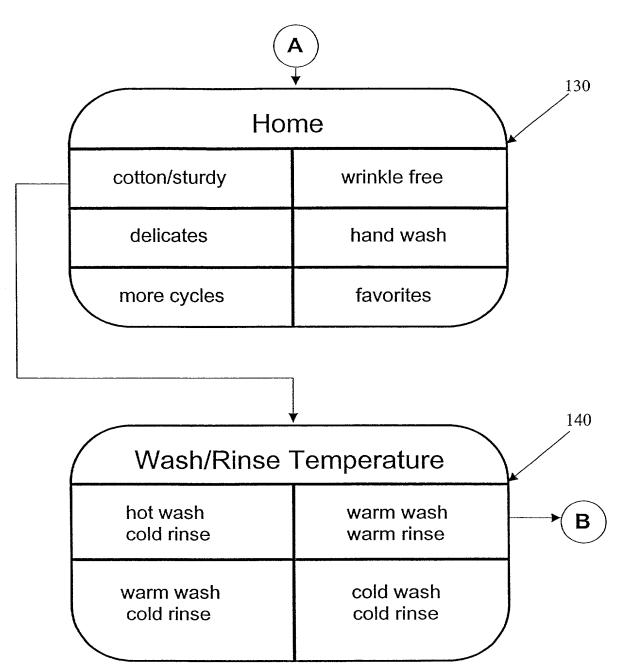


FIG. 3B

